



40V P-CHANNEL ENHANCEMENT MODE MOSFET POWERDI[®]

Product Summary

V _{(BR)DSS}	R _{DS(on) max}	I _D T _A = +25°C		
-40V	11mΩ @ V _{GS} = -10V	-17.0A		
-40 V	15mΩ @ V _{GS} = -4.5V	-14.5A		

Features and Benefits

- 100% Unclamped Inductive Switch (UIS) Test In Production
- Low On-Resistance
- Fast Switching Speed
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- **PPAP Available**

Description

This new generation MOSFET has been designed to minimize the onstate resistance (R_{DS(on)}) and yet maintain superior switching performance, making it ideal for high efficiency power management

Applications

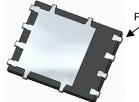
- DC-DC Converters
- Power management functions
- Analog Switch

Mechanical Data

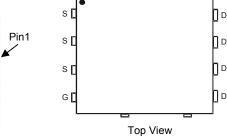
- Case: POWERDI®5060-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish 100% matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.097 grams (approximate)

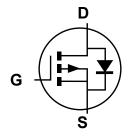


Top View



Bottom View





Internal Schematic Pin Configuration

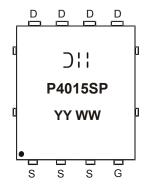
Ordering Information (Note 4 & 5)

Ī	Part Number	Compliance	Case	Packaging
	DMP4015SPSQ-13	Automotive	POWERDI®5060-8	2,500 / Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product_grade_definitions/.
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



☐☐ = Manufacturer's Marking P4015SP = Product Type Marking Code YYWW = Date Code Marking YY = Year (ex: 13 = 2013) WW = Week (01 - 53)



Characteristic	Symbol	Value	Units		
Drain-Source Voltage	V_{DSS}	-40	V		
Gate-Source Voltage	V _{GSS}	±25	V		
Outline Daily Outline (NV)	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	-8.5 -6.8	А
Continuous Drain Current (Note 6) V _{GS} = -10V	t<10s	T _A = +25°C T _A = +70°C	I _D	-13.0 -10.5	А
Continuous Durin Courset (Nato 7) V - 40V	Steady State	T _A = +25°C T _A = +70°C	I _D	-11.0 -8.7	А
Continuous Drain Current (Note 7) V _{GS} = -10V	t<10s	T _A = +25°C T _A = +70°C	l _D	-17.0 -13.5	А
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	I _{DM}	-100	Α		
Maximum Body Diode Continuous Current (Note 7)			Is	-3.5	Α
Avalanche Current (Note 8)			I _{AS}	-22	Α
Avalanche Energy (Note 8)			E _{AS}	242	mJ

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 6)	T _A = +25°C	C	1.3	W
Total Power Dissipation (Note 6)	T _A = +70°C	P_{D}	0.8	
Thermal Resistance, Junction to Ambient (Note 6)	Steady state	C	96.4	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	$R_{\theta JA}$	40.6	°C/W
Total Power Dissipation (Note 7)	$T_A = +25^{\circ}C$	P_{D}	2.1	W
Total Fower Dissipation (Note 1)	$T_A = +70^{\circ}C$	FD	1.4	
Thermal Resistance, Junction to Ambient (Note 7)	Steady state	D	55.0	°C/W
Thermal Resistance, Junction to Ambient (Note 7)	t<10s	$R_{\theta JA}$	24.0	°C/W
Thermal Resistance, Junction to Case (Note 7)	R ₀ JC	4.15	°C/W	
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

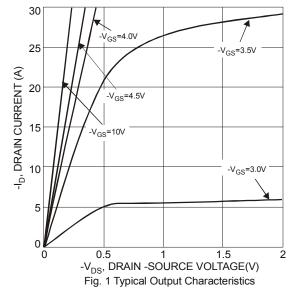
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 9)							
Drain-Source Breakdown Voltage	BV _{DSS}	-40	_		V	$V_{GS} = 0V$, $I_D = -250\mu A$	
Zero Gate Voltage Drain Current	I_{DSS}	_	_	-1	μΑ	$V_{DS} = -40V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 25V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 9)							
Gate Threshold Voltage	$V_{GS(th)}$	-1.5	-2	-2.5	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
Static Drain-Source On-Resistance		_	7	11	mΩ	$V_{GS} = -10V, I_D = -9.8A$	
Static Diain-Source On-Resistance	R _{DS (ON)}	_	9	15	11177	$V_{GS} = -4.5V, I_D = -9.8A$	
Forward Transfer Admittance	Y _{fs}	_	26	_	S	$V_{DS} = -20V, I_{D} = -9.8A$	
Diode Forward Voltage	V_{SD}	_	-0.7	-1	V	V _{GS} = 0V, I _S = -1A	
DYNAMIC CHARACTERISTICS (Note 10)							
Input Capacitance	Ciss		4234	_		V _{DS} = -20V, V _{GS} = 0V f = 1MHz	
Output Capacitance	Coss		1036	_	pF		
Reverse Transfer Capacitance	Crss		526	_			
Gate Resistance	R_{G}	_	7.77	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Q_g		47.5	_			
Gate-Source Charge	Q_{gs}	_	14.2	_	nC	$V_{DS} = -20V, V_{GS} = -5V$ $I_{D} = -9.8A$	
Gate-Drain Charge	Q_{gd}	_	13.5	_			
Turn-On Delay Time	$t_{D(on)}$		13.2	_		V_{GS} = -10V, V_{DD} = -20V, R_{G} = 6 Ω , I_{D} = -1A, R_{L} = 20 Ω	
Turn-On Rise Time	t _r	_	10	_	no		
Turn-Off Delay Time	t _{D(off)}		302.7	_	ns		
Turn-Off Fall Time	t _f	_	137.9	_			

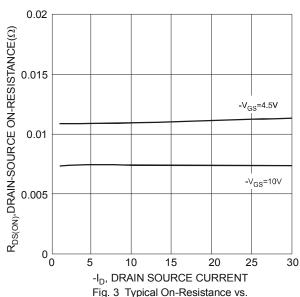
Notes:

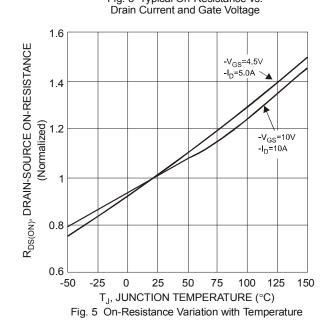
- 6. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
 7. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate
- 8. UIS in production with L = 0.1mH, TJ = +25°C
- 9 .Short duration pulse test used to minimize self-heating effect.
- 10. Guaranteed by design. Not subject to production testing.

POWERDI is a registered trademark of Diodes Incorporated

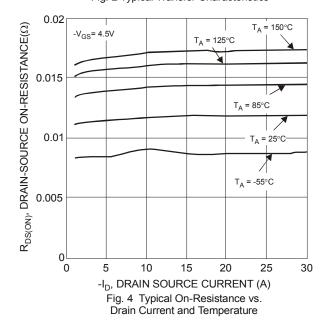


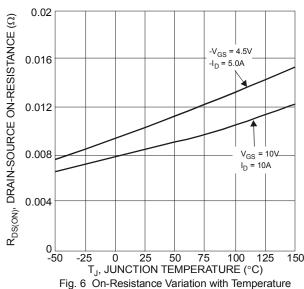






30 T_A = 150°C V_{DS}= -5.0V T_A = 25°C 25 T_A = 125°C -I_D, DRAIN CURRENT (A) 20 15 T_A = 85°C 10 5 0 0.5 1.5 2 2.5 3 3.5 0 4 -V_{GS}, GATE-SOURCE VOLTAGE (V) Fig. 2 Typical Transfer Characteristics





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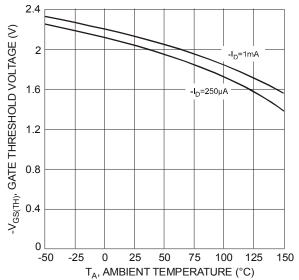
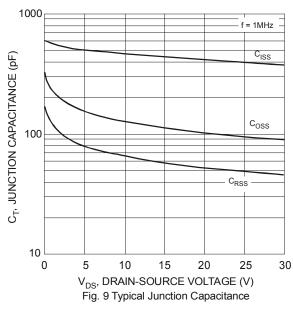
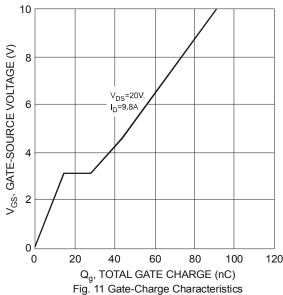


Fig. 7 Gate Threshold Variation vs. Ambient Temperature





30 25 T_A= 25°C -I_S, SOURCE CURRENT (A) 20 15 10 5 0 0 0.2 0.4 0.6 8.0 1.2 1.4 -V_{SD}, SOURCE-DRAIN VOLTAGE (V) Fig. 8 Diode Forward Voltage vs. Current

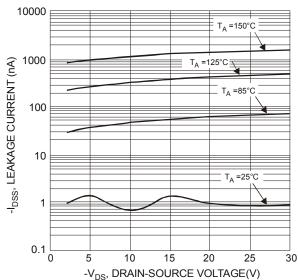


Fig. 10 Typical Drain-Source Leakage Current vs. Voltage

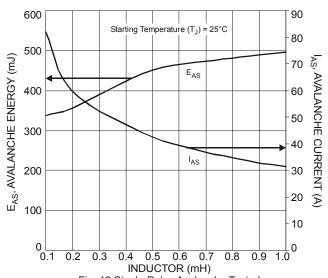
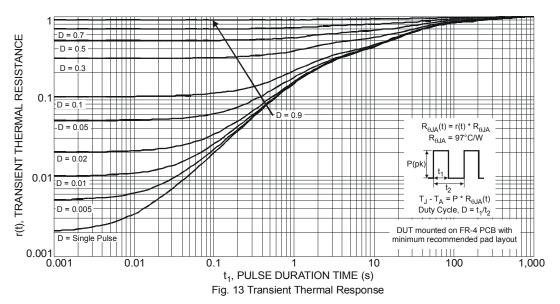


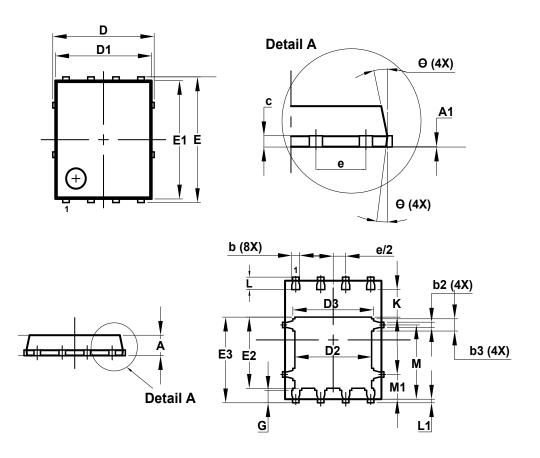
Fig. 12 Single-Pulse Avalanche Tested





Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

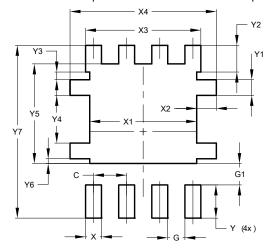


POWERDI5060-8					
Dim	Min	Max	Тур		
Α	0.90	1.10	1.00		
A 1	0.00	0.05	_		
b	0.33	0.51	0.41		
b2	0.200	0.350	0.273		
b3	0.40	0.80	0.60		
С	0.230	0.330	0.277		
D	5	.15 BS	2		
D1	4.70	5.10	4.90		
D2	D2 3.70		3.90		
D3	3.90	4.30	4.10		
Е	6.15 BSC				
E1	5.60	6.00	5.80		
E2	E2 3.28		3.48		
E3	3.99	4.39	4.19		
е	1	.27 BS0	0		
G	0.51	0.71	0.61		
K	0.51	-	_		
L	0.51	0.71	0.61		
L1	0.050	0.20	0.175		
M	M 3.235		3.635		
M1	1.00	1.40	1.21		
Θ	10°	12°	11°		
Θ1	6°	8°	7°		
All Dimensions in mm					



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	1.270
G	0.660
G1	0.820
Х	0.610
X1	4.100
X2	0.755
Х3	4.420
X4	5.610
Y	1.270
Y1	0.600
Y2	1.020
Y3	0.295
Y4	1.825
Y5	3.810
Y6	0.180
Y7	6.610

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